A world-leading research institute in the field of nano electronics that functions through global partnerships with industry leaders in electronics, semiconductor fabrication, healthcare and energy sectors wanted to conduct a prior-art search.

**Requirement**

To conduct a prior-art search to verify the patentability of the client’s disclosed inventions. The inventions were related to CMOS imagers, in particular, methods by which pixel factors such as fill factor and symmetry are maintained.

**Netscribes’ Solution**

1. Conducted global patent searches using a range of search strategies involving permutations of broad and specific keywords for all the features, their synonyms and classification codes.
2. Conducted non-patent searches for the features of the disclosed invention.
3. Refined searches iteratively using elements such as assignees, inventors, and citations.
4. Rigorously reviewed results to identify elements central and peripheral to the scope of the invention.
5. Brief review by the analyst of all the relevant publications and bibliographic details of the results.
6. Electronic copies of patent and non-patent publications.